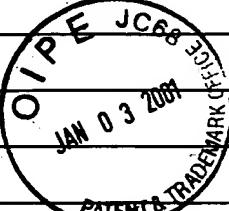


**INFORMATION DISCLOSURE CITATION**  
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Atty. Docket No. 03180.0265				Serial No. 09/661,919	
Applicant	Taku UCHINO				
Filing Date	January 3, 2001			Group	2816
<b>U.S. PATENT DOCUMENTS</b>					
Examiner Initial*	Document Number	Date	Name	Class	Sub Class
<i>TZ</i>	5,966,523	10/12/99	Uchino		
<i>TZ</i>	5,847,966	12/08/98	Uchino et al.		
<b>FOREIGN PATENT DOCUMENTS</b>					
	Document Number	Date	Country	Class	Sub Class
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>					
<i>TZ</i>	NAJM et al.; "Probabilistic Simulation for Reliability Analysis of CMOS VLSI Circuits", IEEE Transactions on Computer-Aided Design, VOL. 9, NO. 4, pp. 439-450, (1990)				
<i>TZ</i>	TSUI et al.; "Efficient Estimation of Dynamic Power Consumption Under a Real Delay Model", International Conference on Computer-Aided Design, pp. 224-228, (1993)				
<i>TZ</i>	UCHINO et al.; "Switching Activity Analysis using Boolean Approximation Method", International Conference on Computer-Aided Design, pp. 20-25, (1995)				
Examiner <i>TZ</i>			Date Considered	<i>5/7/04</i>	
*Examiner:	Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.				
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INFORMATION DISCLOSURE CITATION	Atty. Docket No. 3180.0265	Serial No. 09/661,919
	Applicant Uchino	OIPE FEB 12 2001 TRADEMARK REGISTRATION NO. 8088
	Filing Date September 14, 2000	Group 2816

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*Examiner Initial	Document Number	Date	Country	Class	Sub-Class	Translation Yes	Translation No

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<i>TCJ</i>	Copy of U.S. Patent Application Serial No. 09/312,828
	<i>Now US Patent # 6,272,663</i>
	<i>TCJ</i>

Examiner <i>TCJ</i>	Date Considered <i>5/7/04</i>

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